

Faculty of Engineering & Technology
Department of Electronic & Communication Engineering
Minutes of Meeting
Boards of Studies

A meeting of Boards of Studies of Electronic & Communication Engineering, FET was held on 19/08/2025 (Tuesday) at 2:30 PM. in conference room of FET. The following members were present:

- | | |
|---------------------------|---------------|
| 1. Dr. Indrajeet Gupta | - Chairperson |
| 2. Dr. Samir Kumar Mishra | - Member |
| 3. Dr. Raghvendra Singh | - Member |

The following members agreed to review the minutes in Delhi.

- | | |
|----------------------|-------------------|
| 1. Prof. Jawar Singh | - External Member |
| 2. Dr. Sunil Panday | - External Member |

Agenda:

1. Action Taken Report (ATR) on Minutes of Previous Meeting.

The BOS committee confirmed the minutes of the BOS meeting held on 25/05/2024.

2. To review Result analysis.

The Board reviewed the result analysis and found it to be acceptable but need more improvements.

3. Review of the existing program and their curricula

S. No.	Item No.	Existing	Recommendation /Action Taken
1	No change Syllabus of P.G Course in Electronic & Communication Engineering as per CBCS system admitted in session 2025-26	Marks Based Evaluation System	The BOS discussed the item and Recommended for approval the no change syllabus for the 2020-21 session of P.G Course Electronic & Communication Engineering which is designed as per CBCS system.

The meeting concluded with a vote of thanks to the chair.

Date of the Next Meeting: to be decided and conveyed later

(Chairman)

CC:

1. Dean
2. Registrar Office



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

First Semester

S. NO.	Course Type	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P	CA	MTE	ETE			
1	Program Core	MEPC-101	Semiconductor Device Modeling	3	1	0	30	20	100	150	4	5
2	Program Core	MEPC-102	Architectural Design of ICs	3	1	0	30	20	100	150	4	5
3	Program Elective		Program Elective-1	3	1	0	30	20	100	150	4	5
4	Program Elective		Program Elective-2	3	1	0	30	20	100	150	4	5
PRACTICALS												
5	Program Core	MEPC-151	Logic Design Lab	0	0	4	-	20	30	50	2	2
6	Program Elective		Program Elective-1	0	0	4	-	20	30	50	2	2
			TOTAL	12	4	8	120	120	460	700	20	24

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Second Semester

S. NO.	Course Type	CODE	SUBJE CT	TEACHING SCHEME				EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P		CA	MTE	ETE			
1.	Program Core	MECPC-103	Digital VLSI Circuit	3	1	0		30	20	100	150	4	5
2	Program Core	MECPC-104	Advance Microprocessor & Microcontroller	3	1	0		30	20	100	150	4	5
3	Program Elective		Program Elective-3	3	1	0		30	20	100	150	4	5
4.	Program Elective		Program Elective-4	3	1	0		30	20	100	150	4	5
PRACTICALS													
6	Program Core	MECPC-152	Advance Microprocessor & Microcontroller Lab	0	0	4		-	20	30	50	2	2
7	Program Elective		Program Elective-3	0	0	4		-	20	30	50	2	2
			TOTAL	12	4	8		120	120	460	700	20	24

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Third Semester

S. NO	Course Type	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS
				L	T	P	CA	MTE	ETE		
1.	Open Elective		Open Elective-1	3	1	0	30	20	100	150	4
2.	Open Elective		Open Elective-1	3	1	0	30	20	100	150	4
PRACTICALS											
3.	Dissertation	MECPD 101	Dissertation I	0	0	16	-	200	300	500	12
			TOTAL	6	2	16	60	240	500	800	20

Fourth Semester

S. NO.	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME				TOTAL MARKS	CREDITS
			L	T	P	CA	MTE	ETE			
PRACTICALS											
3.	Project	MECPD 102	Dissertation II	0	0	24	-	200	600	800	20
			TOTAL	0	0	24	00	200	600	800	20

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Program Core

S. NO.	CODE	SUBJECT	TEACHING SCHEME						EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES	
			L	T	P	J	CA	MTE	ETE							
THEORY																
1.	MECPC 101	Semiconductor Device Modeling	3	1	0	0	0	30	20	100	150	4	5			
2.	MECPC 102	Architectural Design of ICs	3	1	0	0	0	30	20	100	150	4	5			
3.	MECPC 103	Digital VLSI Circuit	3	1	0	0	0	30	20	100	150	4	5			
4.	MECPC 104	Advance Microprocessor & Microcontroller	3	1	0	0	0	30	20	100	150	4	5			
PRACTICALS																
8.	MECPC -I51	Logic Design Lab	0	0	4	0	0	-	20	30	50	2	2			
9.	MECPC -I52	Advance Microprocessor & Microcontroller Lab	0	0	4	0	0	-	20	30	50	2	2			

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Program Elective

S. NO.	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME				TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	MTE	ETE					
Bouquet: Elective I														
THEORY														
1	MECPE101	Analog VLSI Design	3	1	0	0	30	20	100	100	150	4	5	
2	MECPE102	Advanced Data Communication Network	3	1	0	0	30	20	100	100	150	4	5	
3	MECPE103	Switching Theory and Logic Design	3	1	0	0	30	20	100	100	150	4	5	
4	MECPE104	CAD of VLSI Design	3	1	0	0	30	20	100	100	150	4	5	
PRACTICALS														
5	MECPE -151	VLSI Design Lab	0	0	4	0	-	20	30	30	50	2	2	

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Program Elective

S. NO.	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	MTE	ETE				
Bouquet: Elective II													
THEORY													
1	MECPE201	VLSI Technology	3	1	0	0	30	20	100	150	4	5	
2	MECPE202	Advanced Signal Processing	3	1	0	0	30	20	100	150	4	5	
3	MECPE203	Artificial Neural Network	3	1	0	0	30	20	100	150	4	5	
4	MECPE204	Nano Technology	3	1	0	0	30	20	100	150	4	5	

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M.Tech Microelectronic & VLSI Design

Program Elective

S. NO.	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME				TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	MTE	ETE					
Bouquet: Elective III														
THEORY														
1	MECPE301	Low Power VLSI Design	3	1	0	0	30	20	100	150	4	5		
2	MECPE302	Memory Technologies	3	1	0	0	30	20	100	150	4	5		
3	MECPE303	Artificial Intelligence	3	1	0	0	30	20	100	150	4	5		
4	MECPE304	Micro-controller based system design	3	1	0	0	30	20	100	150	4	5		
PRACTICALS														
5	MECPE -351	VLSI CAD Lab	0	0	4	0	-	20	30	50	2	2		

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Course Curriculum (w.e.f. Session 2020-21)
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Program Elective

S. NO.	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	MTE	ETE				
Bouquet: Elective IV													
THEORY													
1.	MECPE401	Fault Tolerant Digital Design	3	1	0	0	30	20	100	150	4	5	
2.	MECPE402	Advance Computer Architecture	3	1	0	0	30	20	100	150	4	5	
3.	MECPE403	Pattern Recognition	3	1	0	0	30	20	100	150	4	5	

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Dissertation

S. NO.	CODE	SUBJECT	TEACHING SCHEME					EVALUATION SCHEME			TOTAL CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	EVALUATION SCHEME					
								MTE	ETE				
PRACTICALS													
1.	MECPD 101	Dissertation I	0	0	16	0	-	200	300	500	12	6	
2	MECPD 102	Dissertation II	0	0	24	0	-	200	600	800	20	10	
TOTAL			0	0	40	0	-	400	900	1300	32	0	

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Open Elective -1

S. NO.	CODE	SUBJECT	TEACHING SCHEME					EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	MTE	ETE					
THEORY														
1.	MECOE101	Design of Digital IC	3	1	0	0	30	20	100	150	4	5		
2.	MECOE102	Micro Electro Mechanical System	3	1	0	0	30	20	100	150	4	5		
3.	MECOE103	Information Security	3	1	0	0	30	20	100	150	4	5		
4.	MECOE104	Compound Semiconductors	3	1	0	0	30	20	100	150	4	5		



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

First Semester

S. NO.	Course Type	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACT'S HRS/WK
				L	T	P	CA	MTE	ETE			
1.	Program Core	MECPC-101	Semiconductor Device Modeling	3	1	0	30	20	100	150	4	5
2.	Program Core	MECPC-102	Architectural Design of ICs	3	1	0	30	20	100	150	4	5
3.	Program Elective		Program Elective-1	3	1	0	30	20	100	150	4	5
PRACTICALS												
5.	Program Core	MECPC-151	Logic Design Lab	0	0	4	-	20	30	50	2	2
6.	Program Elective		Program Elective-1	0	0	4	-	20	30	50	2	2
			TOTAL	9	3	8	90	100	360	550	16	19

DEPARTMENT OF ELECTRONIC & COMMUNICATION ENGINEERING, Faculty of Engineering and Technology

Prof. S. S. S.

Dr.

Dr. S. S. S.



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Second Semester

S. NO.	Course Type	CODE	SUBJE CT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P	CA	MTE	ETE			
1.	Program Core	MECPC-103	Digital VLSI Circuit	3	1	0	30	20	100	150	4	5
2.	Program Core	MECPC-104	Advance Microprocessor & Microcontroller	3	1	0	30	20	100	150	4	5
3.	Program Elective		Program Elective-2	3	1	0	30	20	100	150	4	5
PRACTICALS												
6.	Program Core	MECPC-152	Advance Microprocessor & Microcontroller Lab	0	0	4	-	20	30	50	2	2
7.	Program Elective		Program Elective-2	0	0	4	-	20	30	50	2	2
			TOTAL	9	3	8	90	100	360	550	16	19

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Prof. J. S. Jeyaraj

DK

Dr. S. S. Srinivasan



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Third Semester

S. NO.	Course Type	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P	CA	MTE	ETE			
1.	Program Elective		Program Elective-3	3	1	0	30	20	100	150	4	5
2.	Open Elective		Open Elective-1	3	1	0	30	20	100	150	4	5
PRACTICALS												
3.	Program Elective		Program Elective-3	0	0	4	-	20	30	50	2	2
			TOTAL	6	2	0	60	60	230	350	10	12

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Fourth Semester

Sl. No	Course Type	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P		CA	MTE	ETE			
1.	Program Elective		Program Elective-4	3	1	0		30	20	100	150	4	5
PRACTICALS													
3.	Dissertation	MECPD 101	Dissertation I	0	0	8		-	100	200	300	7	10
			TOTAL	3	1	8		30	120	300	350	11	13

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