

Faculty of Engineering & Technology
Department of Electronic & Communication Engineering
Minutes of Meeting
Boards of Studies

A meeting of Boards of Studies of Electronic & Communication Engineering, FET was held on 19/08/2025 (Tuesday) at 2:30 PM. in conference room of FET. The following members were present:

- | | |
|---------------------------|---------------|
| 1. Dr. Indrajeet Gupta | - Chairperson |
| 2. Dr. Samir Kumar Mishra | - Member |
| 3. Dr. Raghvendra Singh | - Member |

The following members agreed to review the minutes in Delhi.

- | | |
|----------------------|-------------------|
| 1. Prof. Jawar Singh | - External Member |
| 2. Dr. Sunil Panday | - External Member |

Agenda:

1. Action Taken Report (ATR) on Minutes of Previous Meeting.

The BOS committee confirmed the minutes of the BOS meeting held on 25/05/2024.

2. To review Result analysis.

The Board reviewed the result analysis and found it to be acceptable but need more improvements.

3. Review of the existing program and their curricula

S. No.	Item No.	Existing	Recommendation /Action Taken
1	No change Syllabus of P.G Course in Electronic & Communication Engineering as per CBCS system admitted in session 2025-26	Marks Based Evaluation System	The BOS discussed the item and Recommended for approval the no change syllabus for the 2020-21 session of P.G Course Electronic & Communication Engineering which is designed as per CBCS system.

The meeting concluded with a vote of thanks to the chair.

Date of the Next Meeting: to be decided and conveyed later

(Chairman)

CC:

1. Dean
2. Registrar Office



S. NO.	Course Type	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME				TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P	CA	MTE	ETE					
1.	Program Core	MEPCPC-101	Semiconductor Device Modeling	3	1	0	30	20	100	150	4	5		
2.	Program Core	MEPCPC-102	Architectural Design of ICs	3	1	0	30	20	100	150	4	5		
3.	Program Elective		Program Elective-1	3	1	0	30	20	100	150	4	5		
PRACTICALS														
5.	Program Core	MEPCPC-151	Logic Design Lab	0	0	4	-	20	30	50	2	2		
6.	Program Elective		Program Elective-1	0	0	4	-	20	30	50	2	2		
			TOTAL	9	3	8	90	100	360	550	16	19		

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Sl. No.	Particulars	Dr	Cr	Total
1	Share Capital		1000	1000
2	Reserves		2000	2000
3	Fixed Assets	1500		1500
4	Current Assets	500		500
5	Liabilities	2000		2000
6	Profit		1000	1000
7	Loss	1000		1000
8	Dividend		1000	1000
9	Interest		1000	1000
10	Depreciation	1000		1000
11	Amortisation	1000		1000
12	Provision	1000		1000
13	Income Tax	1000		1000
14	Capital Gains		1000	1000
15	Capital Losses	1000		1000
16	Income		1000	1000
17	Expenses	1000		1000
18	Profit		1000	1000
19	Loss	1000		1000
20	Dividend		1000	1000
21	Interest		1000	1000
22	Depreciation	1000		1000
23	Amortisation	1000		1000
24	Provision	1000		1000
25	Income Tax	1000		1000
26	Capital Gains		1000	1000
27	Capital Losses	1000		1000
28	Income		1000	1000
29	Expenses	1000		1000
30	Profit		1000	1000
31	Loss	1000		1000
32	Dividend		1000	1000
33	Interest		1000	1000
34	Depreciation	1000		1000
35	Amortisation	1000		1000
36	Provision	1000		1000
37	Income Tax	1000		1000
38	Capital Gains		1000	1000
39	Capital Losses	1000		1000
40	Income		1000	1000
41	Expenses	1000		1000
42	Profit		1000	1000
43	Loss	1000		1000
44	Dividend		1000	1000
45	Interest		1000	1000
46	Depreciation	1000		1000
47	Amortisation	1000		1000
48	Provision	1000		1000
49	Income Tax	1000		1000
50	Capital Gains		1000	1000
51	Capital Losses	1000		1000
52	Income		1000	1000
53	Expenses	1000		1000
54	Profit		1000	1000
55	Loss	1000		1000
56	Dividend		1000	1000
57	Interest		1000	1000
58	Depreciation	1000		1000
59	Amortisation	1000		1000
60	Provision	1000		1000
61	Income Tax	1000		1000
62	Capital Gains		1000	1000
63	Capital Losses	1000		1000
64	Income		1000	1000
65	Expenses	1000		1000
66	Profit		1000	1000
67	Loss	1000		1000
68	Dividend		1000	1000
69	Interest		1000	1000
70	Depreciation	1000		1000
71	Amortisation	1000		1000
72	Provision	1000		1000
73	Income Tax	1000		1000
74	Capital Gains		1000	1000
75	Capital Losses	1000		1000
76	Income		1000	1000
77	Expenses	1000		1000
78	Profit		1000	1000
79	Loss	1000		1000
80	Dividend		1000	1000
81	Interest		1000	1000
82	Depreciation	1000		1000
83	Amortisation	1000		1000
84	Provision	1000		1000
85	Income Tax			



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Second Semester

S. NO.	Course Type	CODE	SUBJE CT	TEACHING SCHEME				EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P		CA	MTE	ETE			
1.	Program Core	MECPC-103	Digital VLSI Circuit	3	1	0		30	20	100	150	4	5
2.	Program Core	MECPC-104	Advance Microprocessor & Microcontroller	3	1	0		30	20	100	150	4	5
3.	Program Elective		Program Elective-2	3	1	0		30	20	100	150	4	5
PRACTICALS													
6.	Program Core	MECPC-152	Advance Microprocessor & Microcontroller Lab	0	0	4		-	20	30	50	2	2
7.	Program Elective		Program Elective-2	0	0	4		-	20	30	50	2	2
			TOTAL	9	3	8		90	100	360	550	16	19

Prof. Suresh Kumar

DK

Shri. Suresh Kumar



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Third Semester

S. NO.	Course Type	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME				TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P	CA	MTE	ETE				
1.	Program Elective		Program Elective-3	3	1	0	30	20	100	150	4	5	
2.	Open Elective		Open Elective-1	3	1	0	30	20	100	150	4	5	
PRACTICALS													
3.	Program Elective		Program Elective-3	0	0	4	-	20	30	50	2	2	
			TOTAL	6	2	0	60	60	230	350	10	12	

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For

Dr.

Dr. S. S. Srinivasan



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Fourth Semester

Sl. No.	Course Type	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
				L	T	P	CA	MTE	ETE			
1.	Program Elective		Program Elective-4	3	1	0	30	20	100	150	4	5
PRACTICALS												
3.	Dissertation	MECPD 101	Dissertation I	0	0	8	-	100	200	300	7	10
			TOTAL	3	1	8	30	120	300	350	11	13

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Signature *Dr.*



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Fifth Semester

S. NO.	COURSE CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK
			L	T	P	CA	MTE	ETE			
1.	Open Elective	Open Elective-1	3	1	0	30	20	100	150	4	5
PRACTICALS											
3.	Dissertation	MECPD 102	0	0	8	-	100	200	300	7	10
		TOTAL	3	1	8	30	120	300	450	11	13

Six Semesters

S. NO.	CODE	SUBJECT	TEACHING SCHEME			EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HRS/WK	
			L	T	P	CA	MTE	ETE				
PRACTICALS												
3.	Project	MECPD 103	Dissertation III	0	0	24	-	200	300	500	16	12
			TOTAL	0	0	24	00	200	300	500	16	12

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Program Core

S. No.			SUBJECT			TEACHING SCHEME					EVALUATION SCHEME			TOTAL MARKS		CREDITS		CONTACTS		PRE-REQUISITES	
						L	T	P	J	CA	MTE	ETE									
THEORY																					
1.	MECPC 101	Semiconductor Device Modeling	3	1	0	0	30	20	100	150	4	5									
2.	MECPC 102	Architectural Design of ICs	3	1	0	0	30	20	100	150	4	5									
3.	MECPC 103	Digital VLSI Circuit	3	1	0	0	30	20	100	150	4	5									
4.	MECPC 104	Advance Microprocessor & Microcontroller	3	1	0	0	30	20	100	150	4	5									
PRACTICALS																					
8.	MECPC -151	Logic Design Lab	0	0	4	0	-	20	30	50	2	2									
9.	MECPC -152	Advance Microprocessor & Microcontroller Lab	0	0	4	0	-	20	30	50	2	2									

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Prof. Ravi

Dr.

Dr. S. S. Srinivas



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Program Elective

Program Elective														
CODE	SUBJECT	TEACHING SCHEME					EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES	
		L	T	P	J	CA	MTE	ETE						
Roboquet Elective I														
THEORY														
1.	MECPE101	Analog VLSI Design		3	1	0	0	30	20	100	150	4	5	
2.	MECPE102	Advanced Data Communication Network		3	1	0	0	30	20	100	150	4	5	
3.	MECPE103	Switching Theory and Logic Design		3	1	0	0	30	20	100	150	4	5	
4.	MECPE104	CAD of VLSI Design		3	1	0	0	30	20	100	150	4	5	
PRACTICALS														
5	MECPE -151	VLSI Design Lab		0	0	4	0	-	20	30	50	2	2	

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Program Elective

Program Elective III															
CODE		SUBJECT		TEACHING SCHEME				EVALUATION SCHEME				TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
				L	T	P	J	CA	MTE	ETE					
Program Elective III															
THEORY															
1.	MECPE201	VLSI Technology		3	1	0	0	30	20	100	150	4	5		
2.	MECPE202	Advanced Signal Processing		3	1	0	0	30	20	100	150	4	5		
3.	MECPE203	Artificial Neural Network		3	1	0	0	30	20	100	150	4	5		
4.	MECPE204	Nano Technology		3	1	0	0	30	20	100	150	4	5		
PRACTICALS															
5.	MECPE -251	Analog IC Design Lab		0	0	4	0	-	20	30	50	2	2		

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Program Elective

COURSE CODE		SUBJECT		TEACHING SCHEME				EVALUATION SCHEME				TOTAL MARKS	CONTACTS PER WK	PRE-REQUISITES
L	T	P	J	CA	MTE	ETE								
Bouquet: Elective II														
THEORY														
1.	MECPE301	Low Power VLSI Design			3	1	0	0	30	20	100	150	4	5
2.	MECPE302	Memory Technologies			3	1	0	0	30	20	100	150	4	5
3.	MECPE303	Artificial Intelligence			3	1	0	0	30	20	100	150	4	5
4.	MECPE304	Micro-controller based system design			3	1	0	0	30	20	100	150	4	5
PRACTICALS														
5.	MECPE -351	VLSI CAD Lab			0	0	4	0	-	20	30	50	2	2

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M.Tech Microelectronic & VLSI Design

Program Elective

S. NO.	CODE	SUBJECT	TEACHING SCHEME				EVALUATION SCHEME			TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES
			L	T	P	J	CA	MTE	ETE				
Bouquet: Elective IV													
THEORY													
1.	MECPE401	Fault Tolerant Digital Design	3	1	0	0	30	20	100	150	4	5	
2.	MECPE402	Advance Computer Architecture	3	1	0	0	30	20	100	150	4	5	
3.	MECPE403	Pattern Recognition	3	1	0	0	30	20	100	150	4	5	

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Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Dissertation

Dissertation													
TEACHING SCHEME			BASIC SKILLS			TOTAL CREDITS			CONTACTS / HR/WK			PRE-REQUISITES	
SUBJECT			L T P J			CA MTE ETE			TOTAL MARKS				
PRACTICALS													
1.	MECPD 101	Dissertation I	0	0	8	0	-	100	200	300	7	10	
2.	MECPD 102	Dissertation II	0	0	8	0	-	100	200	300	7	10	
2.	MECPD 103	Dissertation III	0	0	24	0	-	200	600	800	16	12	
		TOTAL	0	0	40	0	-	400	1000	1400	30	0	

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Prof. Dr. B. S. Srinivas

Dr.

Srinivas



Course Curriculum (w.e.f. Session 2020-21)
M.Tech Microelectronic & VLSI Design

Open Elective -1

Sl. No.	COURSE CODE	COURSE TITLE	EVALUATION SYSTEM					TOTAL MARKS	CREDITS	CONTACTS HR/WK	PRE-REQUISITES		
			L	T	P	J	CA					MTET	ETE
THEORY													
1.	MECOE101	Design of Digital IC	3	1	0	0	30	20	100	150	4	5	
2.	MECOE102	Micro Electro Mechanical System	3	1	0	0	30	20	100	150	4	5	
3.	MECOE103	Information Security	3	1	0	0	30	20	100	150	4	5	
4.	MECOE104	Compound Semiconductors	3	1	0	0	30	20	100	150	4	5	

DEPARTMENT OF ELECTRONIC & COMMUNICATION ENGINEERING, Faculty of Engineering and Technology

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