



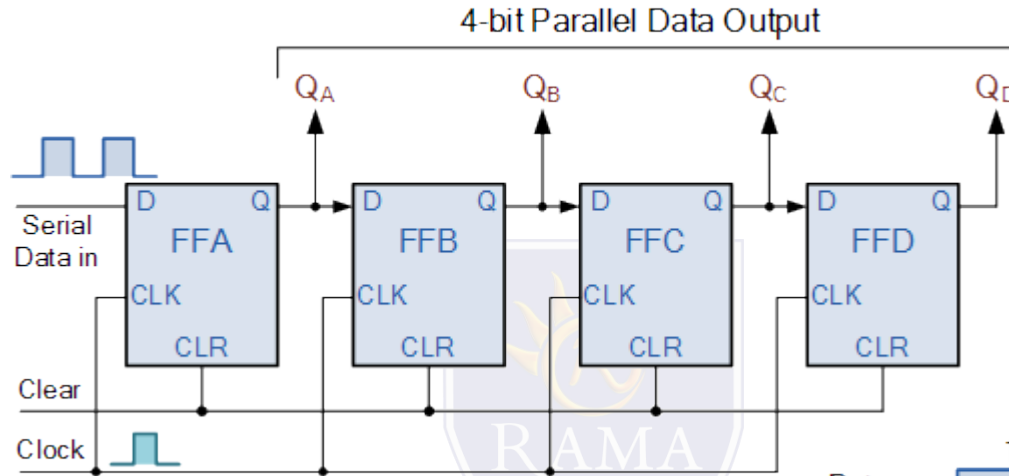
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SHIFT REGISTERS

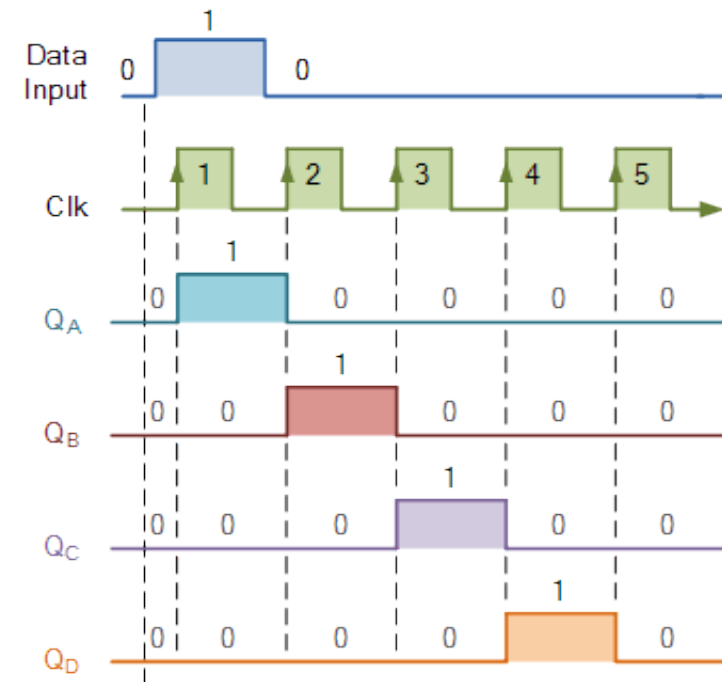
1. Serial-in to Parallel-out (SIPO) Shift Register

4-bit Serial-in to Parallel-out Shift Register



Basic Data Movement Through A Shift Register

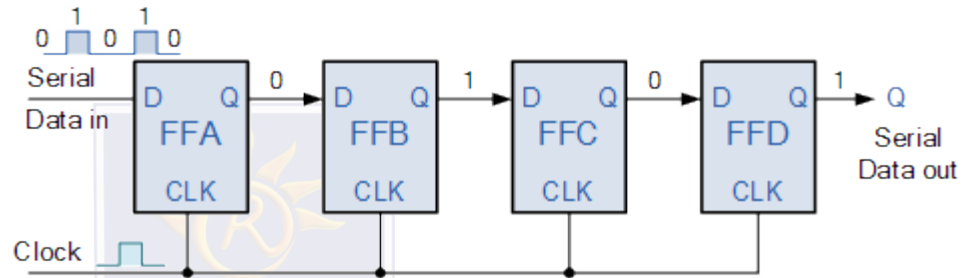
Clock Pulse No	QA	QB	QC	QD
0	0	0	0	0
1	1	0	0	0
2	0	1	0	0
3	0	0	1	0
4	0	0	0	1
5	0	0	0	0



SHIFT REGISTERS

2. Serial-in to Serial-out (SISO) Shift Register

4-bit Serial-in to Serial-out Shift Register



3. Parallel-in to Serial-out (PISO) Shift Register

4-bit Parallel-in to Serial-out Shift Register

